# Quantum Lattice Enumeration in Limited Depth 

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- The concrete hardness of the shortest vector problem (SVP) is at the core of the security estimations for lattice-based primitives
- The cost of SVP solvers is often the leading term in the cost of algorithms for solving lattice problems
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- So far, all cryptographically relevant solvers are classical routines
- At least two of these, sieving and enumeration, can be "compiled" into quantum algorithms using black-box methods [LMv13, KMPM19, ANS18, BCSS23]
- While the resulting asymptotic quantum speedups are understood, there's not a lot of work on their concrete cost; only sieving has been explored [AGPS20]

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- Q. Enum algorithms were first demonstrated by Aono et al. [ANS18]; asymptotically, they provide a quadratic speedup
- Our work looks at the "max-depth" setting, where quantum computation is noisy, and long serial computation causes memory to "decohere" [Nat16, Pre18]
- Our results suggest that, as is the case for Grover search against block ciphers [JNRV20], quantum speedups in this setting may not apply


## Quantum computation

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- This is a quantum circuit of width 3 , depth 5 and gate count 5 .
- Here the wires are qubits, the nodes are gate evaluations.
- The cost of a circuit can be expressed in terms of different metrics, e.g. by counting wires, components, depth, area...
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- $M D=2^{96} \approx$ "gates that atomic scale qubits with speed of light propagation times could perform in a millennium"

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- Grover search parallelises badly [Zal99], causing the concrete quantum advantage to strongly reduce [JNRV20].


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- Conceptually, enumeration consists of depth-first search on a tree $T$ containing short vectors as leaves
- As used in lattice reduction, in dimension $n$, this requires poly $(n)$ memory, and $\mathbb{E}[\# T]=2^{\frac{1}{8} n \log n+o(n)}$ time on average $\left[\mathrm{ABF}^{+} 20\right]$
- Given vectors $\left(b_{1}, \ldots, b_{n}\right)$, let $\pi_{i}\left(b_{j}\right)$ be the part of $b_{j}$ orthogonal to $b_{1}, \ldots, b_{i-1}$
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We can see this as searching for a "marked leaf" in a tree, where a leaf is marked if its norm is $\leq R$.

A look at the enumeration tree $T$


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- The tree size can be somewhat reduced by "pruning" nodes that are unlikely to yield a marked leaf


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- By performing decision on every level, DetectMV $\mapsto$ FindMV, which returns such a leaf
- For trees with one (randomly distributed) marked leaf and $\# T \approx \mathcal{T}$ :

Classical average-case runtime $O(\# T) \mapsto$ quantum average case $\tilde{O}(\sqrt{\# T \cdot n})$

Montanaro's tree search

$$
\begin{gathered}
\operatorname{DF}(\mathcal{T}) \text { times } \quad \mathrm{QD}(\mathcal{T}) \text { times } \mathrm{WQ}(\mathcal{T}, \mathcal{W}) \text { times } \\
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- Under conservative estimations, we serially evaluate $\sqrt{\# T \cdot n}$ times $W$ per QPE
- Our objective is to lower-bound the gate-cost of FindMV $(T)$, while keeping the serial quantum depth within max-depht $M D$

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- Finally, we check if the resulting circuit depth of QPE is $\leq M D$

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\underset{\substack{\text { random } \\ \text { tree } T}}{\mathbb{E}}[\operatorname{Depth}(\operatorname{QPE}(W))] \approx \mathbb{E}[\sqrt{\# T \cdot \beta}] \approx \sqrt{\mathbb{E}[\# T] \cdot \beta} \approx \begin{cases}2^{90.3} & \text { for Kyber-512, } \\ 2^{166.2} & \text { for Kyber-768 } \\ 2^{263.7} & \text { for Kyber-1024 }\end{cases}
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- I do know Jensen's inequality!

$$
\mathbb{E}[\sqrt{\# T}] \leq \sqrt{\mathbb{E}[\# T]}
$$

- Just wait a handful of slides
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- Precompute nodes up to level $k>1$, run FindMV on the subtrees.
- We can estimate the size of subtrees with similar techniques as for the full tree.


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$\Longrightarrow$ total gate-count $\approx H_{n / 2} \approx \operatorname{cost}$ of classical enumeration
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## Disclaimer

qRAM (a.k.a. QRACM) may be quite costly to access [JR23]. Yet, many quantum-classical speedups assume it.

One last step: expected square roots

- We are trying to estimate or lower-bound $\mathbb{E}[\sqrt{\# T}]$, but the distribution of $\# T$ is unknown (Aono et al. [ANS18] already mention this issue)

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## Definition: Multiplicative Jensen's gap

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- Ideally, we want an upper bound to $z$; up to $\beta=70$ we measure $z \approx 1$
- Without such bounds, we can run attack cost estimates as a function of $z$, and see at what point the hypothetical attack becomes viable

Summarising, we obtain formulae for

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## Quantum gate-cost

$$
\begin{aligned}
\underset{\substack{\text { random } \\
\text { tree } T}}{\mathbb{E}}[\text { Quantum Gates }] & \approx \frac{H_{k}}{2^{y}} \cdot \mathbb{E}[\operatorname{Gates}(\operatorname{FindMV}(T(g)))] \\
& \geq \frac{H_{k}}{2^{y}} \cdot \mathbb{E}[\sqrt{\# T(v) \cdot(n-k+1)}] \cdot \operatorname{Gates}(W) \\
& =\frac{H_{k}}{2^{y}} \cdot \frac{1}{2^{z}} \sqrt{\mathbb{E}[\# T(v) \cdot(n-k+1)]} \cdot \operatorname{Gates}(W)
\end{aligned}
$$

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- We report $z, k$ minimising classical + quantum gate-cost
more likely to be feasible
less likely to be feasible

| MD | Kyber | $\log \mathbb{E}[\mathrm{GCost}]$ (with $\mathcal{W}$ as in § 4.1) below... |  |  | $\log \mathbb{E}[\mathrm{GCost}]$ (with $\mathcal{W}$ as in § 4.2) below... |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Target security | $\begin{gathered} \text { Grover on } \\ \text { AES }_{\{128,192,256\}} \end{gathered}$ | $\begin{aligned} & \text { Quasi-Sqrt } \\ & 1 / b \sqrt{\# \mathcal{T} \cdot h} \end{aligned}$ | Target security | $\begin{gathered} \text { Grover on } \\ \text { AES }_{\{128,192,256\}} \end{gathered}$ | $\begin{aligned} & \text { Quasi-Sqrt } \\ & 1 / b \sqrt{\# \mathcal{T} \cdot h} \end{aligned}$ |
| $2^{40}$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ |  |  |  |  |  |  |
| $2^{64}$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ |  |  |  |  |  |  |
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| $\infty$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ | $\begin{aligned} & z \geq 0, k=0 \\ & z \geq 0, k=0 \\ & z \geq 9, k=0 \end{aligned}$ | $\begin{gathered} z \geq 9, k=0 \\ z \geq 52, k=0 \\ z>64 \end{gathered}$ | $\begin{aligned} & z \geq 1, k=0 \\ & z \geq 1, k=0 \\ & z \geq 1, k=0 \end{aligned}$ | $\begin{aligned} & z \geq 0, k=0 \\ & z \geq 1, k=0 \\ & z \geq 35, k=0 \end{aligned}$ | $\begin{gathered} z \geq 33, k=0 \\ z>64 \\ z>64 \end{gathered}$ | $\begin{aligned} & z \geq 26, k=0 \\ & z \geq 27, k=0 \\ & z \geq 28, k=0 \end{aligned}$ |

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| $2^{40}$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ |  |  |  |  |  |  |
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| $2^{96}$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ | $\begin{gathered} z \geq 0, k \leq 58 \\ z \geq 23, k \leq 106 \end{gathered}$ | $\begin{gathered} z \geq 8, k \leq 53 \\ z \geq 56, k \leq 62 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 1, k \leq 58 \\ z \geq 36, k \leq 77 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 0, k \leq 63 \\ z \geq 40, k \leq 77 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 33, k \leq 54 \\ z>64 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 25, k \leq 58 \\ z \geq 52, k \leq 77 \\ z>64 \end{gathered}$ |
| $\infty$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ | $\begin{aligned} & z \geq 0, k=0 \\ & z \geq 0, k=0 \\ & z \geq 9, k=0 \end{aligned}$ | $\begin{gathered} z \geq 9, k=0 \\ z \geq 52, k=0 \\ z>64 \end{gathered}$ | $\begin{aligned} & z \geq 1, k=0 \\ & z \geq 1, k=0 \\ & z \geq 1, k=0 \end{aligned}$ | $\begin{aligned} & z \geq 0, k=0 \\ & z \geq 1, k=0 \\ & z \geq 35, k=0 \end{aligned}$ | $\begin{gathered} z \geq 33, k=0 \\ z>64 \\ z>64 \end{gathered}$ | $\begin{aligned} & z \geq 26, k=0 \\ & z \geq 27, k=0 \\ & z \geq 28, k=0 \end{aligned}$ |

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$\log \mathbb{E}[\mathrm{GCOST}]$ (with $\mathcal{W}$ as in § 4.1) below...


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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $2^{64}$ | $\begin{array}{r} -512 \\ -768 \\ -1024 \end{array}$ | $\begin{gathered} z \geq 0, k \leq 83 \\ z \geq 39, k \leq 114 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 13, k \leq 64 \\ z \geq 57, k \leq 77 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 14, k \leq 59 \\ z \geq 52, k \leq 77 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 11, k \leq 96 \\ z \geq 55, k \leq 111 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 29, k \leq 63 \\ z>64 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 30, k \leq 63 \\ z>64 \\ z>64 \end{gathered}$ |
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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | -512 | $z \geq 7, k \leq 92$ | $z \geq 13, k \leq 83$ | $z \geq 26, k \leq 59$ | $z \geq 23, k \leq 96$ | $z \geq 29, k \leq 79$ | $z \geq 42, k \leq 63$ |
| $2^{40}$ | $\begin{array}{r} -768 \\ -1024 \end{array}$ | $\begin{gathered} z \geq 51, k \leq 114 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 57, k \leq 106 \\ z>64 \end{gathered}$ | $\begin{gathered} z \geq 64, k \leq 77 \\ z>64 \end{gathered}$ | $\begin{aligned} & z>64 \\ & z>64 \end{aligned}$ | $\begin{aligned} & z>64 \\ & z>64 \end{aligned}$ | $\begin{aligned} & z>64 \\ & z>64 \end{aligned}$ |
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|  | -1024 | $z>64$ | $z>64$ | $z>64$ | $z>64$ | $z>64$ | $z>64$ |


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|  | -1024 | $z>64$ | $z>64$ | $z>64$ | $z>64$ | $z>64$ | $z>64$ |
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| $\infty$ | -768 | $z \geq 0, k=0$ | $z \geq 52, k=0$ | $z \geq 1, k=0$ | $z \geq 1, k=0$ | $z>64$ | $z \geq 27, k=0$ |
|  | -1024 | $z \geq 9, k=0$ | $z>64$ | $z \geq 1, k=0$ | $z \geq 35, k=0$ | $z>64$ | $z \geq 28, k=0$ |

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Yet, we can't fully exclude it without a clear understanding of the Jensen gap.

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Can we say anything about it?

Reasons to hope Q. Enum doesn't work:

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Reasons to hope Q. Enum doesn't work:

- In our numbers we observe that the cost reduces smoothly as a funciton of $z$ $\Longrightarrow$ approximate estimates may already help
- Experimental evidence up to $\beta=70$ says $z \approx 1$
- We can prove lower bounds on $\mathbb{E}[\sqrt{\# T}]$ based on the additive and multiplicative Jensen's gaps, implying:

But both depend on $\mathbb{V}[\# T]$.

Open problems

- Not much analysis on $\mathbb{V}[\# T]$


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$$
\# T=\sum_{k=1}^{n}\left|Z_{k}\right|=\sum_{k=1}^{n}\left|\operatorname{Ball}_{k}(\mathbf{0}, R) \cap \operatorname{Lat}\left(\pi_{n-k+1}\left(b_{n-k+1}\right), \ldots, \pi_{n-k+1}\left(b_{n}\right)\right)\right|
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$$
\underset{\substack{\text { random } \\ \text { tree } T}}{\mathbb{V}}\left[\mid \text { Ball }_{k}\left(\mathbf{0}, R_{k}\right) \cap \pi_{n-k+1}(\Lambda) \mid\right] ?
$$

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## Open problems

- We've only covered cylinder pruning. What about discrete pruning? Or ad-hoc pruning for quantum enumeration?
- Currently searching for attack costs is an optimisation problem. Can we find a closed formula? This would allow running it as part of "estimator" scripts.
- There quite a few places where our analysis may not be tight, meaning actual costs are likely higher.


## Conclusions

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## Thank you

Slides @ https://fundamental.domains

Martin R. Albrecht, Shi Bai, Pierre-Alain Fouque, Paul Kirchner, Damien Stehlé, and Weiqiang Wen. Faster enumeration-based lattice reduction: Root hermite factor $k^{1 /(2 k)}$ time $k^{k / 8+o(k)}$. In Daniele Micciancio and Thomas Ristenpart, editors, CRYPTO 2020, Part II, volume 12171 of LNCS, pages 186-212. Springer, Heidelberg, August 2020.

Yoshinori Aono, Thomas Espitau, and Phong Q. Nguyen.
Random lattices: Theory and practice.
Preprint, available at https://espitau.github.io/bin/random_lattice.pdf.
Martin R. Albrecht, Vlad Gheorghiu, Eamonn W. Postlethwaite, and John M. Schanck.
Estimating quantum speedups for lattice sieves.
In Shiho Moriai and Huaxiong Wang, editors, ASIACRYPT 2020, Part II, volume 12492 of LNCS, pages 583-613. Springer, Heidelberg, December 2020.

Yoshinori Aono, Phong Q. Nguyen, and Yixin Shen.
Quantum lattice enumeration and tweaking discrete pruning.
In Thomas Peyrin and Steven Galbraith, editors, ASIACRYPT 2018, Part I, volume 11272 of LNCS, pages 405-434. Springer, Heidelberg, December 2018.

Yoshinori Aono, Phong Q. Nguyen, Takenobu Seito, and Junji Shikata.
Lower bounds on lattice enumeration with extreme pruning.
In Hovav Shacham and Alexandra Boldyreva, editors, CRYPTO 2018, Part II, volume 10992 of LNCS, pages 608-637. Springer, Heidelberg, August 2018.

Xavier Bonnetain, André Chailloux, André Schrottenloher, and Yixin Shen.
Finding many collisions via reusable quantum walks - application to lattice sieving.

In Carmit Hazay and Martijn Stam, editors, Advances in Cryptology - EUROCRYPT 2023-42nd Annual International Conference on the Theory and Applications of Cryptographic Techniques, Lyon, France, April 23-27, 2023, Proceedings, Part V, volume 14008 of Lecture Notes in Computer Science, pages 221-251. Springer, 2023.

Shi Bai, Maya-Iggy van Hoof, Floyd B. Johnson, Tanja Lange, and Tran Ngo.
Concrete analysis of quantum lattice enumeration.
In Advances in Cryptology - ASIACRYPT 2023, Lecture Notes in Computer Science. Springer-Verlag, 2023.
Nicolas Gama, Phong Q. Nguyen, and Oded Regev.
Lattice enumeration using extreme pruning.
In Henri Gilbert, editor, EUROCRYPT 2010, volume 6110 of LNCS, pages 257-278. Springer, Heidelberg, May / June 2010.

Samuel Jaques, Michael Naehrig, Martin Roetteler, and Fernando Virdia.
Implementing grover oracles for quantum key search on AES and LowMC.
In Anne Canteaut and Yuval Ishai, editors, EUROCRYPT 2020, Part II, volume 12106 of LNCS, pages 280-310. Springer, Heidelberg, May 2020.

Samuel Jaques and Arthur G. Rattew.
Qram: A survey and critique, 2023.
Samuel Jaques and John M. Schanck.
Quantum cryptanalysis in the RAM model: Claw-finding attacks on SIKE.
In Alexandra Boldyreva and Daniele Micciancio, editors, CRYPTO 2019, Part I, volume 11692 of LNCS, pages 32-61. Springer, Heidelberg, August 2019.

Elena Kirshanova, Erik Mårtensson, Eamonn W. Postlethwaite, and Subhayan Roy Moulik. Quantum algorithms for the approximate k-list problem and their application to lattice sieving. In Steven D. Galbraith and Shiho Moriai, editors, ASIACRYPT 2019, Part I, volume 11921 of LNCS, pages 521-551. Springer, Heidelberg, December 2019.

Thijs Laarhoven, Michele Mosca, and Joop van de Pol.
Solving the shortest vector problem in lattices faster using quantum search.
In Philippe Gaborit, editor, Post-Quantum Cryptography - 5th International Workshop, PQCrypto 2013, pages 83-101. Springer, Heidelberg, June 2013.

Ashley Montanaro.
Quantum-walk speedup of backtracking algorithms.
Theory Comput., 14(1):1-24, 2018.
National Institute of Standards and Technology.
Submission requirements and evaluation criteria for the Post-Quantum Cryptography standardization process.
http://csrc.nist.gov/groups/ST/post-quantum-crypto/documents/
call-for-proposals-final-dec-2016.pdf, December 2016.
John Preskill.
Quantum Computing in the NISQ era and beyond.
Quantum, 2:79, August 2018.
Christof Zalka.
Grover's quantum searching algorithm is optimal.

Phys. Rev. A, 60:2746-2751, Oct 1999.


[^0]:    ${ }^{0}$ Image courtesy of Sam Jaques.

[^1]:    ${ }^{0}$ Image courtesy of Sam Jaques.

